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Substitute for form 1448A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	10/673,874
				Filing Date	29 SEP 93
				First Named Inventor	Harry Contopanagos
				Group Art Unit	2823
Sheet 1		of	2	Examiner Name	William M. Brewster
				Attorney Docket Number	BP 2108

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code 2 (if known)			

FOREIGN PATENT DOCUMENTS								
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		Office 3	Number 4	Kind Code 5 (if known)				
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Examiner Signature	William M. Brewster	Date Considered	27 MAY 94
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1 Unique citation designation number. 2 See attached Kinds of U.S. Patent Documents. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. 6 Applicant is to place a check mark here if English language Translation is attached

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Substitute for form 1449A/PTO			Complete if Known		
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			Group Art Unit	2823	
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Sheet	2	of	2	Attorney Docket Number	BP 2108

Examiner Initials *	Cite No. 1	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
WB	1	"PROGRESS IN RF INDUCTORS ON SILICON - UNDERSTANDING SUBSTRATE LOSSES" by Joachim N. Burghartz - IBM Research Division, T. J. Watson Research Center, P. O. Box 218, Yorktown Heights, New York 10598 USA	<input type="checkbox"/>
WB	2	IEEE Journal of Solid-State Circuits, Vol. 33, No 5, May 1998 - the "Stanford Paper" - "ON-CHIP SPIRAL INDUCTORS WITH PATTERNED GROUND SHIELDS FOR SI-BASED RF IC's - C.Patrick Yue, Student Member, IEEE, and S. Simon Wong, Senior Member, IEEE	<input type="checkbox"/>
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